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New directions for integrated circuit cards operating systems

Pierre Paradinas, Jean-Jacques Vandewalle

January 1995 ACM SIGOPS Operating Systems Review, Volume 29 Issue 1
Publisher: ACM

Full text available: pdf(422.64

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 29, Citation Count: 0

Integrated circuit cards or smart cards are now well-known. Applications such as electronic purses (cash units stored in cards), subscriber identification cards used in cellular telephone or access keys for pay-TV and information highways emerge in many ...

Keywords: integrated circuit card applications, integrated circuit card operating system, object-oriented technologies, secured method execution

2 New directions for integrated circuit cards operating systems

Pierre Paradinas, Jean-Jacques Vandewalle

September EW 6: Proceedings of the 6th workshop on ACM SIGOPS European workshop: Matching operating systems to application needs

Publisher: ACM

Full text available: pdf(437.96 KB) Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): 5. Downloads (12 Months): 22. Citation Count: 0

Integrated circuit cards or smart cards are now well-known. Applications such as electronic purses (cash units stored in cards), subscriber identification cards used in cellular telephone or access keys for pay-TV and information highways emerge in many ...

Keywords: Integrated Circuit Card Applications, Integrated Circuit Card Operating System, Object-Oriented Technologies, Secured method execution

3 Silicon physical random functions

Blaise Gassend, Dwaine Clarke, Marten van Dijk, Srinivas Devadas November CCS '02: Proceedings of the 9th ACM conference on Computer and 2002 communications security

Publisher: ACM

Full text available: pdf(433.69 Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 84, Citation Count: 6

We introduce the notion of a Physical Random Function (PUF). We argue that a complex integrated circuit can be viewed as a silicon PUF and describe a technique to identify and authenticate individual integrated circuits (ICs). We describe several possible ...

Keywords: identification, physical random function, physical security, smartcard, tamper resistance, unclonability

4 Simulation models for side-channel information leaks

Kris Tiri, Ingrid Verbauwhede

June DAC '05: Proceedings of the 42nd annual conference on Design automation 2005

Publisher: ACM

Full text available: pdf(244.28 Additional Information: full citation, abstract, references, cited by,

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 74, Citation Count: 2

Small, embedded integrated circuits (ICs) such as smart cards are vulnerable to socalled side-channel attacks (SCAs). The attacker can gain information ...

Keywords: countermeasure, differential power analysis, encryption, security IC. side-channel attack, simulation model, smart card

Delay-based circuit authentication and applications

Blaise Gassend, Dwaine Clarke, Marten van Dijk, Srinivas Devadas March SAC '03: Proceedings of the 2003 ACM symposium on Applied computing

2003

Publisher: ACM

Full text available: pdf(869.99 KB)

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 12, Citation Count: 0

We describe a technique to reliably identify individual integrated circuits (ICs), based on a prior delay characterization of the IC. We describe a circuit architecture for a key card for which authentication is delay based, rather than based on a digital ...

Keywords: physical random function, physical security, smartcard, tamper resistance, unclonability

Autocheck program, program to check validity of printed circuit cards and circuit

usage

Donald J. McGinnis

ACM'72: Proceedings of the ACM annual conference - Volume 1, 1972 Volume 1

Publisher: ACM

Full text available: pdf(1.30 MB)

Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 12, Citation Count: 0

The Autocheck program is used to validate the routing of etched wiring on PC cards for the present generation of computer systems under development. Additionally, it validates the manual assignments of logic gates to integrated circuits, the pin assignments ...

7 Multilayer printed wiring—computer aided design

M. Freeman, G. Ginsberg, M. Resnick, E. Whitley

January 1967 DAC '67: Proceedings of the 4th conference on Design automation **Publisher:** ACM

Full text available: Additional Information: full citation, abstract, references, cited by, index

Bibliometrics: Downloads (6 Weeks): 0. Downloads (12 Months): 5. Citation Count: 2

Integrated circuits, as compared to discrete components, have greatly increased the amount of circuitry that can be mounted on a printed wiring card. Also, board assemblies can be mounted close together, 0.4 inch and less, by taking advantage of low ...

8 Automatic test-generation and test-verification of digital systems

J. P. Verma, D. M. Selove, J. N. Tessier

January 1974 DAC '74: Proceedings of the 11th workshop on Design automation

Publisher: IEEE Press

Full text available: additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 11, Citation Count: 6

The widespread use of large scale and medium scale integrated circuits, coupled with the trend towards larger boards, made manual generation of test patterns very expensive, somewhat ineffective, and rather difficult to update for design changes. The ...

9 Test generation systems in Japan

S. Funatsu, N. Wakatsuki, T. Arima

January 1975 DAC '75: Proceedings of the 12th conference on Design automation **Publisher:** IEEE Press

Full text available: pdf(597.15 Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 16, Citation Count: 12

With the advent of large scale and medium scale integrated circuit, test and diagnosis of digital logic circuits become more and more difficult to get an efficient and economical goal. In this paper, Test Generation Systems for testing digital logic ...

10 An automated method for designing logic circuit diagnostic programs

Masaaki Nagamine

June 1971 DAC '71: Proceedings of the 8th workshop on Design automation Publisher: ACM

Full text available: pdf(288.18

Additional Information: full citation, abstract, references, cited by, index terms

KB)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 23, Citation Count: 1

This presentation describes a method for generating functional test programs for Large Scale Integrated Circuits (LSI) and Logic Cards having hundreds of logic gates. J. P. Roth's D-Algorithm, and S. Seshu's Logic Simulation Method have both been used...

11 A study of responsive electronic vehicular instrumentation

Joseph S. Nadan

January 1971 WSC '71: Proceedings of the 5th conference on Winter simulation **Publisher:** ACM

Publisher: ACM

Full text available: pdf(781.59

Additional Information: full citation, abstract, index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 26, Citation Count: 0

The advent of low-cost ultrareliable integrated circuits has made feasible the design of a responsive electronic vehicular instrumentation system that results in significant improvement in the surveillance of highway internal performance factors; e.g. ...

12 in praise of computing

D. A. B. Lindberg

December 1987 Proceedings of ACM conference on History of medical informatics Publisher: ACM

Full text available: pdi(499.50 KB)

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 33, Citation Count: 0

I shall describe briefly some early work in applying computing systems to clinical laboratory, diagnosis, and medical records problems. If we compare the early work with what can be done now, we shall see many examples of enormous improvements in speed ...

13 Distributed processing in manufacturing at GTE Automatic Electric

Ralph J. Moses

June 1978 DAC '78: Proceedings of the 15th conference on Design automation **Publisher:** IEEE Press

Full text available: pdf(521.14

Additional Information: full citation, abstract, index terms (KB)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 15, Citation Count: 0

As a manufacturer of complex electronic telephone switching systems, we at GTE Automatic Electric have faced a number of problems in manufacturing, particularly in the area of data control and data collection. As is typical of most electronic products. ...

14 ACM SIGCOMM computer communication review

Andrew T. Campbell, Mischa Schwartz

October ACM SIGCOMM Computer Communication Review, Volume 31 Issue 5 2001

Publisher: ACM

Full text available: pdf(376.25 KB)

Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 6. Downloads (12 Months): 37. Citation Count: 0

At some point in the future, how far out we do not exactly know, wireless access to the Internet will outstrip all other forms of access bringing the freedom of mobility to the way we access the web, communicate with each other, and conduct business. ...

15 Protected transmission of biometric user authentication data for oncard-matching

Ulrich Waldmann, Dirk Scheuermann, Claudia Eckert

March SAC '04: Proceedings of the 2004 ACM symposium on Applied computing 2004

Publisher: ACM

Full text available: pctf(574.45

Additional Information: full citation, abstract, references, cited by

Bibliometrics: Downloads (6 Weeks): 22, Downloads (12 Months): 212, Citation Count: 1

Since fingerprint data are no secrets but of public nature, the verification data transmitted to a smartcard for oncard-matching need protection by appropriate means in order to assure data origin in the biometric sensor and to prevent bypassing the ...

Keywords: authentication, biometrics, cryptographic protocols, data integrity, electronic signature, oncard-matching, smartcards, system security, tamper proof environment

16 An extended model of security policy for multi-applicative smart cards

Matthias Schwan

March ASIACCS '07: Proceedings of the 2nd ACM symposium on Information, 2007 computer and communications security

Publisher: ACM

Full text available: pdf(211.79

Additional Information: full citation, abstract, references, index terms

KB)

Bibliometrics: Downloads (6 Weeks): 30. Downloads (12 Months): 390. Citation Count: 0

The overall security level of applications running on a smart card is not only determined by the particular applications and by the operating system but also by external devices. All proposed security models of smart card operating systems assume the

Keywords: biometrics, electronic signatures, security policy, smart cards

17 Security and privacy for the next wireless generation

Pascal Urien, Guy Puiolle

March International Journal of Network Management, Volume 18 Issue 2

2008

Publisher: John Wiley & Sons, Inc.

Full text available: pdf(736.37

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 0, Citation Count: 0

This paper presents research performed during the last 4 years (2003-2007), in order to design applications (usually referred as EAP smart cards) which enhance security and privacy in emerging WLAN infrastructures. It introduces smart cards ensuring ...

18 SIM and USIM filesystem; a forensics perspective

Antonio Savoldi, Paolo Gubian

March SAC '07: Proceedings of the 2007 ACM symposium on Applied computing 2007

Publisher: ACM

Full text available: pdf(167.67

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 25. Downloads (12 Months): 333. Citation Count: 0

The main purpose of this paper is to describe the real filesystem of SIM and USIM cards, enlightening what the official standard reference does not say. By analyzing the full filesystem of such embedded devices, it is possible to find a lot of undocumented ...

Keywords: SIM/USIM card, data hiding, filesystem, filesystem analysis, imaging tool

19 Secure access to IP multimedia services using generic bootstrapping architecture (GBA) for 3G & beyond mobile networks

Muhammad Sher, Thomas Magedanz

October Q2SWinet '06: Proceedings of the 2nd ACM international workshop on 2006 Quality of service & security for wireless and mobile networks

Publisher: ACM

Full text available: pdf(541.17

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 34, Downloads (12 Months): 805, Citation Count: 0

The IP Multimedia Subsystem (IMS) defined by Third Generation Partnership Projects (3GPP and 3GPP2) is a technology designed to provide robust multimedia services across roaming boundaries and over diverse access technologies with promising features ...

Keywords: IP multimedia system, authentication proxy, generic authentication architecture, generic bootstrapping architecture, security and privacy, transport layer security

20 Implementing identity provider on mobile phone

Tsuyoshi Abe, Hiroki Itoh, Kenji Takahashi

November DIM '07: Proceedings of the 2007 ACM workshop on Digital identity 2007 management

Publisher: ACM

Full text available: pdi(2.09

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 64. Downloads (12 Months): 203. Citation Count: 0

We have implemented an identity provider (IdP), which is defined by the Liberty Alliance on a mobile phone. We propose an authentication method, which uses this personal IdP as a security token to prevent password leakage. In our method, the personal ...

Keywords: authentication, federated identity, identity provider, mobile phone

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